

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al.

Art Unit : 2183

Serial No. : 10/070,091

Examiner : Eric Coleman

Filed : June 28, 2002

Conf. No. : 7309

Title : REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR
ARCHITECTURE

MAIL STOP RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Please consider the references listed on the enclosed PTO-1449 form. Foreign patent documents and non-patent literature are enclosed; cited U.S. patents and patent application publications will be provided on request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: January 9, 2009

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I hereby certify that this paper was filed with the Patent and Trademark Office using the EFS-WEB system.